

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	142	(double adj gate adj field adj effect adj transistor) or (double adj FET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 06:00
L2	95	(double adj gate adj FET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 06:00
L3	67	2 not 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 06:31
L4	27754	level adj shift\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 06:31
L5	5	(2 or 3) and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 06:33
L6	5	2 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 06:32
L7	5548179	divid\$4 or resist\$4 or diode\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 06:35
L8	93	7 and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 06:36

L9	1	1 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 06:36
L10	0	8 not (1 or 2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 06:37
L11	0	8 not 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 06:37
L12	74	8 not 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 06:49

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	242	(dual or double) adj: (FET or (field adj effect adj transistor))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:49
L2	27757	level adj shift\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:50
L3	12	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:57
L4	1040	dual adj gate adj ((field adj effect adj transistor) or FET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:51
L5	34	2 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:58
L6	180	double adj gate adj ((field adj effect adj transistor) or FET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:52
L7	5	6 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:59
L8	5653	(double or dual) SAME FET	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:53

L9	230	8 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:53
L10	2289908	(double gate FET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:54
L11	17380	10 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:54
L12	279722	predetermined adj (magnitude or time or ris\$5 or fall\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:56
L13	2013	12 and 11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:56
L14	1989	13 not 3 not 5 not 7 not 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/22 08:56